

DATA SHEET

Y2 AC Ceramic Capacitor 250VAC

Serie: I22003

Mat. Code	E	Material: B= Y5P
Voltage Code	251	Voltage: 251= 250VAC
Range Code	472	Range: 472= 4700pf

										Y2 AC Cera 250	mic Capacitor VAC
										Serie No.:	122003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	01.11.2010	Customori	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	1 from 13	Customer.	
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Temperature Range:

Code

101

102

222

103

Capacitance Tolerance:

Temperature Characteristics

Capacitance Change of Temperature

Coeffizient

Technical Specifications

Y5P = +10%

K= ± 10%

 $M = \pm 20\%$

Capacitance (pf)

Nominal Capacitance Code (Example)

100

1000

2200

10000 Nominal capacitance shall consist of three numbers in the unit of picofard(pf). The frist and

the second numbers mean the signifibant figures and the third number shall presendt the number of zeros flowing the significant figures.

Y5P and Y5U and Y5V

Y5U = ±20% ~ -55%

 $Y5V = \pm 30\% \sim -80\%$. -25°C ~ +85°C





Lead Style Informations



Lead Code Style (A) (mm)

Pitch Code	Α	В	С	D	E				
F	2,5 5,0 7,5 10 12								
L	only 20mm long lead								
d	0,5 or 0,6 or 0,8mm								
е		ma	ax. 4,0n	nm					

										Y2 AC Ceran 250	nic Capacitor VAC
										Part No.:	122003
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REACH **RoHS** Lead Free



Lead Style Informations

Lead Code Style (B) Unit (mm)

С

7,5

5,0mm or on customer request

0,5 or 0,6 or 0,8mm

max. 4,0mm

D

10

В

5,0

А 2,5 L:5±1

Ε

12,5

Pitch Code

F

А

L

d

Lead Style Informations

Lead Code Style (C) Unit (mm)

С

7,5

5,0

5,0mm or on customer request

0,5 or 0,6 or 0,8mm

D

10

6.5

Е

12,5

6.5

В

5,0

5.0



Pitch Code

F

L

d

е



Lead Style Informations



Lead Code Style (D) Unit (mm)

Pitch Code		В	С	D	E				
F		5,0	7,5	10	12,5				
A		5,0	5,0	6,5	6,5				
L	20mm min.								
d		0,5 or	0,6 or (),8mm					

										Y2 AC Ceram 250	nic Capacitor /AC
										Part No.:	I22003
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Lead Style Informations

Lead Style Informations





Lead Code Style (M) Unit (mm)

Lead Code Style (H) Unit (mm)

Pitch Code		В	С	D	Е				
F	5,0 7,5 10 12,								
L	5,0mm or on customer request								
d	0,5 or 0,6 or 0,8mm								

	В	С	D	ш			
	5,0	7,5	10	12,5			
	2,6	2,6	3,3	3,3			
	1,3	1,25	1,65	1,65			
	1,65	1,65	1,95	1,95			
D<8	3: 6,0±	1,5, D>	•8: 7,0±	: 1,5			
3,0 ~ 30mm							
	0,5 or	0,6 or (),8mm				
	 D<8	B 5,0 2,6 1,3 1,65 D<8: 6,0± 3,1 0,5 or	B C 5,0 7,5 2,6 2,6 1,3 1,25 1,65 1,65 D<8: 6,0±	B C D 5,0 7,5 10 2,6 2,6 3,3 1,3 1,25 1,65 1,65 1,65 1,95 D<8: 6,0±			

Y2 AC Ceramic	Capacitor
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250VAC

t No.:	122003

										Part No.:	122003
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Specification and test method

Operating Temperature range -25°C ~ +105°C But temperature range is -25% ~ +85°C at safety standard specification.

Test and measurement shall be made at the standard condition. (Temperature 15 ~ 35°C relative humidity 45 ~ 75% and athmospheric pressure 860~1060hpa). Unless otherwise specified herein it doubt accurated on the value of measurement, and remesuarement was requested by customer capacitor shall be measuremed at the reference condition (Temperature 20 ±2°C, relative humidity 60~70% and atmospheric pressure 860~1060hpa). unless otherwise specified herein.

Impedance vs. Frequency Characteristics



Leakage Current Characteristics

REACH

AC voltage : 60Hz Temperature : 25°C

HINF100

AC voltage [V(r.m.s.)]

HMF472MODO

HMF222MDDD HMF102MDDD

MODO

2000

3000

RoHS Lead Free



Capacitance Temperature Characteristics

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Char:F (Y5V)



Temperature (°C) Y2 AC Ceramic Capacitor 250VAC

	Fr	requency (MHz)								Part No.:	122003
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Type HM (F/Y5V char.)

6.D

4.0

3.0 ā

0.0

Ď

[mA(r.m.s.)] 5.0

8 2.0

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1000



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Sheet No.

	ltem		Specification		Testing Me	thod			Item		Speci	ication	Т	esting Method
Ар	perance and	No ma	rked defect on apper	rance The	capacitor shall be irsp eyes for visible evider	pected by nacked nce of defect.				Cha	r. Capac	tance Change	The capaci made of ea	itance measurement shall be ach step specified in table 3.
C	Dimensions	iiom a	specified range.	Din	mensions shall be mea	asured with slide				В	v	vithin ± 10%	Step	Temperature (°C)
			-1		calipers.			Те	emperature	E	with	in + 20% -55%	1	.+ 20 ±2
	Marking	-	To be easily legible	The	capacitor shall be irsp	pected by nacked		Ch	aracteristics	F	with	in + 30% -80%	2	25 ±2
	Marking		to be easily legible.		eyes					Tom	noraturo	characteristics	3	.+ 20 ±2
С	apacitance	Wi	thin spefied toleranc	e _	ha aanaaitanaa diaai	nation shall be				qua	arantee is	-25 to +85°C	4	.+ 85 ±2
		(Char. Specification	mea	asured at 25 + 2°C wit	h 1 + 0.1KHz and				Ű			5	.+ 20 ±2
Dissipa	ation Factor (D,F)	В	, E= D,F= ≤ 2,5%		$AC1 \pm 0.1V$ (r	:.m.s)								diachargo in made E0 timos
			F= D,F= ≤ 5,0%			-			Apperance		No mark	ed defect.	5sec interval	s from the capacitor (Cd)
Insulatio	on Resistance (R)		10000M Ω min.	The wi	insulation resistance s ith DC 500 ± 50V with charging	shall be measured in 60 ±5sec. Of			I.R.		1000M	1Ωmin.	charged at D	C voltage of specified
	Between Lead No failure		The AC :	e capacitor shall not b 2600V (r.m.s.) are app lead wires for		le test (1)					- Vs -	G CL SR2		
Dielectric Strength	Body Insulation		No failure	First, conne right, the bo 3-4mi sins file	the terminals of the capa ected together. Then as s a metal foil shall be close ody of the capacitor to the m from each terminal. The capacitor shall be setedinto a container ed with ballsof about a diameter. Eigally AC	Acitor shall be shown in Figure ely wrapped around e distance of about een the	-	Discharg	Dielectric Strength		per l	tem 6.	Ct: Capacitor Cd: 0,001μF S: high voltag R1: 1000Ω R2: 1000MΩ R3: Surge res Vs: DC 10KV	Fig.1 under Test ge switch sistance
				AC2 fc cap	2600(r.m.s.) is applied or 60s between the vacitor lead wires and metal balls.	Metal bolis]						Y2 AC O	Ceramic Capacito 250VAC : I22003
DF	RW: Ja	son	CHKD	Wilson	MATL:	Wilson	TOLE	RANCE	Mason	DA	ATE	01.11.2010	Customer.	
							1					1 a a		

FINISH

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Jamy







A single layer of cheese cloth is to be placed around the body of the test capacitor. Each sample is to be subjected to four dicharge from a dump capacitor charged to a voltage that. When discharged, placed DC 5KV across the capacitor under test. The interval between successive discharge is to be 5s. AC240V (r.m.s.)-60Hz potential is to applied across the capacitor under test andis to be maintained for 30s. after the fouth discharge, unless the circuit is opened in a shorter time by breakdown of the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Solderability of leads Lead wire shall be soldered with uniformly coated on the axial direction. 2,0mm from the root of lead wires. 2,0mm from the root of lead wires. The cheese-cloth around The cheese-cloth around The capacitor shall firmly be solde a with the following. The cheese-cloth around The cheese-cloth around to the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Solderability of leads Lead wire shall be soldered with uniformly coated on the axial direction. 2,0mm from the root of lead wires. 10 to 55Hz in the vibration frequent 10 to 55Hz in the vibration freq	Item	Specifi	cation	Testing M	ethod		Item	Speci	fication	Testing N	lethod
A single layer of cheese cloth is to be placed around the body of the test capacitor. Each sample is to be subjected to four dicharges from a dump capacitor charged to a voltage that. When discharged, placed DC 5KV across the capacitor under test. The interval between successive discharge is to be 5s. AC240V (r.m.s.)-60Hz potential is to applied across the capacitor under test and is to be maintained for 30s. after the fouth discharge, unless the circuit is opened in a shorter time by breakdown of the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Discharge Trest II The cheese-cloth around cpacitors shall not glow or flame. Cap. Value Cd to 0,005µF 0,0,0,05µF 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,		•		, , , , , , , , , , , , , , , , , , ,						Capacitance value an	d D.F. are follows.
capacitor. Each sample is to be subjected to four dicharges from a dump capacitor charged to a voltage that. When discharged, placed DC 5KV across the capacitor under test. The interval between successive discharge is to be 5s. AC240V (r.m.s.)-60Hz potential is to applied across the capacitor under test andis to be maintained for 30s. after the fouth discharge, unless the circuit is opened in a shorter time by breakdown of the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Lead wire shall be soldered with uniformly coated on the axial direction over 3/4 of the circuit for 30s. after the fouth discharge, unless the circuit is opened in a shorter time by breakdown of the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. No market defect The capacitor shall firmly be soldered with uniformly coated on the axial direction. The capacitor shall firmly be solder of 235 ± 5°C for The depth of immersion is up to ab 2,0mm from the root of lead wires. The cheese-cloth around The coapacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Apperance No market defect The capacitor shall firmly be solde supporting lead wire and vibration 10 to 55Hz in the vibration frequent.				A single layer of cheese placed around the body of	cloth is to be of the test	Dis	charge Trest II	The cheese-clot	h around cpacitors	Cap. Value Cd to 0, 0,05	005µF 0,0051 to ıF
four dicharges from a dump capacitor charges from a dump capacitor D.F of Cd. 0,5% max. 0,5% charged to a voltage that. When discharged, placed DC 5KV across the capacitor under test. The interval between successive discharge is to be 5s. AC240V (r.m.s.)- 60Hz potential is to applied across the capacitor under test andis to be maintained for 30s. after the fourt discharge, unless the circuit is opened in a shorter time by breakdown of the capacitor. The direct No market defect The capacitor shall firmly be solde Within the specified tolerance Capacitance Char. Specification The capacitor shall firmly be solde into to 55Hz in the vibration frequent 10 to 55Hz in the vibration frequent				capacitor. Each sample i	s to be subjected to			Shair not g	low of fiame.	Cap. Value CD 0,00)5μF 0,05μF
The cheese-cloth around The cheese-cloth around The cheese-cloth around The cheese-cloth around				four dicharges from a du	mp capacitor					D.F of Cd. 0,5% m	nax. 0,5%max.
Apperance No market defect Within the specified tolerance The cheese-cloth around				placed DC 5KV across the test. The interval betwee discharge is to be 5s. AC 60Hz potential is to appli capacitor under test andi	e capacitor under n successive :240V (r.m.s.)- ed across the s to be maintained	Sold	erability of leads	Lead wire shall uniformly coa direction o circumferer	l be soldered with ated on the axial ver 3/4 of the ntial direction.	The lead wire of capacit into molten solder of 23 The depth of immersion 2,0mm from the root of	or shall be dipped $5 \pm 5^{\circ}$ C for 2 ± 0.5 s. is up to about 1.5 to lead wires.
breakdown of the capacitor. The direct current supply is to be adjusted to provide a potential in accordance with the following. Within the specified tolerance The cheese-cloth around The cheese-cloth around The cheese cloth around The cheese cloth around				circiut is opened in a sho	rter time by		Apperance	No mar	ket defect		
The cheese-cloth around current supply is to be adjusted to provide a potential in accordance with the following. 0 Capacitance Char. Specification Supporting lead wire and vibration 10 to 55Hz in the vibration frequential				breakdown of the capacit	or.The direct		Apperance	Within the spe	ecified tolerance	The conceitor shall firm	ly be coldered to the
		The cheese-	cloth around	current supply is to be ac potential in accordance v	ljusted to provide a vith the following.	istance	Capacitance	Char. S	Specification	supporting lead wire ar 10 to 55Hz in the vibrat	id vibration which is ion frequency range.
Discharge Trest II cpacitors shall not glow or flame. 1,5mm in total amplitude, and about the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each in 3 mutually perpendicular the rate of vibration change from 55Hz and back to 10Hz is applied of 6H; 2H each	Discharge Trest II	cpacitors shall not glow of flame.		Vdc= $\frac{5000(Cd+Ct)}{Cd}$ (V)		Vibration Resi	D, F.	B, E F	D,F, ≤ 2,5% D,F, ≤ 5,0%	1,5mm in total amplitud the rate of vibration ch 55Hz and back to 10Hz of 6H; 2H each in 3 mu direction	e, and about 1min in ange from 10Hz to is applied for a total tually perpendicular ons.
Fig.2 Vdc: Varaible direct-current voltage source. s: High voltage switch				Fig Vdc: Varaible direct-curre s: High voltage switch	1.2 ent voltage source.						
L: Choke coil of appr. 3mH and 0,03Ω				L: Choke coil of appr. 3m	H and 0,03Ω						
F: Plug fuse rated 30A and 250V				F: Plug fuse rated 30A a	nd 250V						
Vac.: supply source rated 240V 60Hz 30A Y2 AC Ceramic Capa				Vac.: supply source rated	240V 60Hz 30A					Y2 AC Cerami	c Capacitor
C1: Capacitor under test.				C1: Capacitor under test						∠50V	AC
Cd: Dump Capacitor Part No.: 122003				Cd: Dump Capacitor				1		Part No.:	122003
DRW: Jason CHKD Wilson MATL: Wilson TOLERANCE Mason DATE 01.11.2010 Customer:	DRW: Ja	ason CHI	KD Wi	Ison MATL:	Wilson	TOLERANCE	Mason	DATE	01.11.2010	Customer:	
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	ltem	Specification	Testing Method					
	Apperance	No marked defect	As in figure, the lead wires shall be immersed solder of $350 \pm 10^{\circ}$ C or $260 \pm$					
	Capacitance change	Within ± 10%	5°C up to 1,5 ~ 2,0mm from the root of the terminal for 3,5 \pm 0,5s. (10 \pm 1s for 260 \pm					
	I.R.	1000M Ω min.						
Soldering Effect	Dielectric Strength	Pre Item 6.	Pre-treatment: Capacitor shall be stored at 85 ± 2°C for 1h. Then placed at room conditions for 24 ± 2h before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2 h ar room conditions.					

	ltem		Specification	Testing Method
e)	Appearance		No marked defect.	
Stat	Conscitores	Char	. Capacitance Change	
dy S	Capacitance	В	within ± 10%	
rea	onunge	E,F	within ± 15%	Set the capacitor for 500 \pm 12h at 40 \pm 2°C
r St		Char. Specification		in 90 ~ 95% relative humidity. Post-
nde	D,F,	B,E	D.F. ≤ 5,0%	treatment: Capacitor shall be stored for 1 to
U)		F	D.F. ≤ 7,5%	2h at room condition.
dity	I.R.		3000M Ω min.	
Humi	Dielectric Strength		Per Item 6	
	Appearance	l	No marked defect.	
	Oranitaria	Char	. Capacitance Change	
D	Capacitance	В	within ± 10%	
adir	onunge	E,F	within ± 15%	Apply the rated voltage for $500 \pm 12h$ at 40
Loi		Char.	Specification	± 2°C in 90 ~ 95% relative humidity. Post-
dity	D,F,	B,E	D.F. ≤ 5,0%	treatment: Capacitor shall be stored for 1 to
imi		F	D.F. ≤ 7,5%	2h at room condition.
Ť	I.R.		3000M Ω min.	
	Dielectric Strength		Per Item 6	

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	ltem	Specification	Testing Method		Item	Specif	ication	Testing Method
	Appearance	No marked defect.	Impulse Voltage			The capacitor flan	ne discontinue as	The Capacitor shall be subjected to applied for 15 s
	Capacitance Change	Within ± 20%	Each individual Capacity shal be subjected			Cycle	Time	until 5 cycle.
	I.R.	3000M Ω min.	capacitance are supplied to life test.			1 to 4	30s max.	
	Dielectric Strength	Per Item 6.	100/%)	Flame Test		5 60s. Max		Fiame
۵			90 11=1.2 μ s=1.671 12=50 μ s 30 0 +T+					Bas Burner (in mm)
Life			<	ess of ation	Tensile	Lead wire shall		As a figure, fix the body of capacitor apply a
	Discharge Test (II)	Per Item 9.	Apply a voltage of table 4 for 1000h at 105 +2/0°C, and relative humidity of 50% max. (table 4)	Robustne Termina	Bending	Capacitor shall noit be broken.	Π.	the radia direction of capacitor up to $10N$ and keep it for $10\pm 1s$.
			Applied Voltage AC 425V (r.m.s.). Except that once each hour the oltage is increased to AC 1000V (r.m.s.) for 0,1s. Post-treatment: Cpapcitor shall be stared for 1 to 2h at room temperature.	Active	e Flammability	The chees-cloth fir	shall not be on e.	Each lead wire shall be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then a 90° bend in the opposite direction at the rate of one bend in 2 to 3s.

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	Item	Sp	ecification		Testing	Metho	d		
	Appearance	No r	marked defect	T 1	e eltere els elles es de	·	F (
	Quantitation	Char.	Capaci.Change	The cap	acitor snall be suc	Jected to	5 temperature		
	Capacitance	В	Within ± 10%	oyonoo, anon concocantory to <u>_</u>			icision cycles.		
	onange	E;F	Within ± 20%		Temperature cycle				
				Step	Temperature	e (°C)	Time		
				1	25 +0/-3		30min		
e		Char.	Specification	2	Room temper	rature	3min		
Styl	DE	B;E	D.F. ≤ 5,0%	3	.+ 105 +3	8/0	30min		
uo	D.I .	F	D.F. ≤ 7,5%	4	Room temper	rature	3min		
nd Immers					Immersion o	Cycle tin cycle	ne: 5cycle		
ature and I	I.R.	30	000M Ω min.	Step	Temperature (°C)	Time	Immersion Water		
empera				1	. +65 +/-0	15min	Clean Water		
	Dialactria			2	Room Temp.	15min.	Salt Water		
	Strength		Per Item 6	Pre-tratr for 1h, th	nent: Capacitor s nenplaced at room	hall be sto condition	ored at 85 \pm 2°C ns for 24 \pm 2h.		
				Post-treatment: Capacitor shall be stored for $24 \pm 2h$ at room conditions.					

"Room Condition" Temperature 15 to 35°C, Relative humidity; 45 to 75%, Atmospheric pressure: 6 to 106KPa.

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APPD:	Schumi			FINISH	Jamy		Shee	t No.	11 from 13	Cusioner.	

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Ordering Informations

Serie		Range	Temperature Character.	Voltage	Tolerance Code	Lead Style Code	Lead Length Code	Lead Space Code	ROHS	Packing Code		
	_											-
122003	-	472	E	251	М	Α	20	D	R	BU		
	4	472= 4700pf		251= 250VAC	M= 20%	A= Style A	20= 20mm	A= 2,50mm	R= ROHS Conform	BU= Bulk Ware		
			E= Y5U			B= Style B	05= 5mm / ±1mm	B= 5,00mm	N= NON ROHS	TA= Tape Ammo Pac	k	
						C= Style C		C= 7,50mm	Conform	TR= Tape Reel		
						D= Style D		D= 10,0mm				
						H= Style H		E= 12,5mm				
						M= Style M						
										Y2	AC Ceramic 250VA	Capacit C
										Pa	art No.:	122003
DRW: APPD:	Jaso Schu	on CH mi	KD Wil	son MA FIN	NL: Wi	Ison TOLE	RANCE Ma	son DA Sheet No	TE 01.11 12 fr	0m 13	omer:	
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Soldering Profile Curve

Classification Reflow Profile (JEDEC J-STD-020C)

