







DATA SHEET

Super High Voltage Disc Ceramic Capacitor

Serie: 123006

Range 102= 1000pf

Tolerance K= ±10%

Voltage 15000 Volt

Material Character. 5P

Body Diam. 20,0mm

Pitch 10mm

Body Thickn. 10,0mm

Super High Voltage Disc Ceramic Capacitor Serie No.: 123006

DRW: Jason CHKD Wilson MATL: Wilson **TOLERANCE** Mason DATE 30.04.2011 APPD: Schumi **FINISH** Sheet No. Jamy 1 from 14

email: info@edcon-components.com

Customer:









Features

Wide rated Voltage range, wide nominal capacitance range Flame retardent, insulating coating applied Recomended Application Filter circuit of high voltage power High voltage circuit of television set and monitor High voltage circuit of various electronic equipment

Characteristics	Temp.Char. SL Temp.Char. Y5P Temp.Char. Y5U					Temp.Char. Y5V				
Operating Temperature		30°C ~ +85°C								
Rated Voltage	4KVDC ~ 6KVDC	4KVDC ~ 15KVDC	4KVDC ~	15KVDC	4KVDC ~	15KVDC				
Withstanding Voltage		1,5 times re	lated voltage		-					
Capacitance	Within the speci	fied tolerance, testing at 25°0	C, 1Vrms and 1	KHz (at 1MHz	for SL produc	ts)				
Capacitance	10 ~ 330pf	100 ~ 2200pf	470 ~ 3300pf		1000 ~ 10000pf					
Dissipation Factor	Cr<30pf, Q≥ 400+20Cr Cr≥30pf, Q≥1000	tg ≤ 2,5%		tg≤	3,5%					
Insulation Resistance		Charge at 500VDC for 6	0 seconds, Rj	≥ 1000MΩ						
Tomporatura	Temperatur Cha	Temperatur Chararcteristics Code			Y5U	Y5V				
Temperature Characteristics	Temperatur Coe	fficient (10-6 /°C)	. +100 ~ - 1000 10-6/°C	. ± 10%	.+22 ~ +56%	.+22 ~ +82%				

Rated Capacitance

The first and second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier. The capacitance unit is pf,

Capacitance Tolerance

Letter Symbol	Capacitance Tolerance	Letter Symbol	Capacitance Tolerance
С	±0,25pf	K	±10%
D	±0,5pf	M	±20%
J	±5%	Z	.+80 ~ -20%

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Part No.: **123006**

Customer:









Standard atmospheric condition

Temperature: 15~35°C Relative Humidity: 45~75%

Atmospheric pressure: 86~106KPa (860~1060mbar

Operating and storage temperature range

Operating Temperature:

Temperature	Lowest Operating	Highest Operating		
•				
Characteristics	Temperature	Temperature		
SL	25°C	.+85°C		
COH	25°C	.+85°C		
Y5P	25°C	.+85°C		
Y5U	25°C	.+85°C		
Y5U	25°C	.+85°C		
Y5V	25°C	.+85°C		
Z5U	10°C	.+85°C		
Z5V	10°C	.+85°C		
YR	25°C	.+125°C		

Storage Temperature Range: -10 to + 40°C

Characteristics and test methods

Electrical characteristics and test methods

Jamy			Cl	t No.	3 from 14	Customer:		
Wilson	TOLER	RANCE	Mason	DATE	30.04.2011	Customor		
		,	wrapped on env	elope for 1 to 5 s	seconds.	•	gh Voltage Disc ic Capacitor 123006	
Voltage Pr	roof	540V a 1000V t 3000V shall be voltage 1300 betw	and 500V) 200% to 2000V), 175% V), or 150% rated e applied betwee s of 250% rated V (fort 500V, 11 veen leads conn	rated voltage (for rated voltage (for rated voltage (for d Voltage (for Do en leads for 1 to voltage (for 50) KV and over) sha ected together a	or rated voltage or rated voltage or SBBLC) 5seconds. The capacitors or all be applied nd metal foil	No breakdown or flashover		
Insulatio Resistand		voltage	(for Vr≤500VDC	te shall be meas (c); 500VDC (for 5seconds of cha	1000M Ω min Ω mir	1000N n (for SBBLC)		
Quality factories dissipation for			quality factor o asured at the s	2,5% max. (f 0,5% 3,5% max. 5%max. (for \$. (for Y5P,Y5U and Z5U 5% max. (for YR) ax. (for Y5V and Z5U) or SBBLC Y5V and Y5U) ax. (for SBBLC Y5P)			
						Q≥100	20Cr (forCr<30pf) 00 (forCr<30pf) eacitance in unit of pf	
Capacitano tolerano		and 1Vi	ms (Class1), 1k	citance shall be measured at 25°C with 1Mhz s (Class1), 1KHz and 1Vrms (class II),1KHz Refer to individual sheet ms (for Calss III)				

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	The capacitor shall be kept for enough time to reach thermal equilibrium at special temperature of each step in the following table.								
	The capacitance measurement shall be made only at the thermal equilibrium of each step.	Cla	ass I						
	Step Temperature Step Temperature	Te	emperature coefficient:						
	1 20 ± 2°C 4 85 ±2°C (125±2°C for YR)								
	225 ± 2°C 5 20 ± 2°C	Ca	apacitance drift:						
	3 $20 \pm 2^{\circ}$ C		ithin ±1% or ± 0,05pf						
	For temperature characteristics SL the steps 1 and step 2 may be omitted.	(W	/hichever is greater)						
	The temperature coeffizient and the capacitance drift shall be calculated by the following formulas. (Cm - Co)								
	=	Cla	ass II & III						
Temperature	Co (T- To) (ββιίί σ)	T	emperature Permitting						
Characteristics	$C_0 - C_1$ $C_5 - C_0$ $C_5 - C_1$	(Characteris capacitance						
Characteriotics	= or		tics change						
	Co Co Co	Y5							
	Where	YR							
	Co Capacitance at step 3	Y5							
	Cm Capacitance at step 2 and/or step 4	Z5							
	C1,C5 Capacitance at step 1 and step 5	Y5							
	To Measuring temperature at Step 3	Z5	5V ± 22% to -82%						
	T Measuring temperature at Step 2 and /or step 4 Pre-tratement:								
	The capacitor shall be stored at a temperature of 55 ±2°C and a relative humidity of 20% or less for 16 to 24 hours.								
	And then the capacitor shall be allowed immediately to cool in container using appropriate dryer such as activated carbon, silica gel								
	The capacitor body shall be held in such a manner so that axis of the lead is vertical. The tensile force of 10N (for Ø 0,6mm	n lead)	The capacitor shal be no						
Robustness of	ot 5N (for Ø 0,5mm lead) shall be applied to the lead in a direction of ist axis and acting in a direction away from the body	,	proken and the lead shall be						
Termination	capacitor for 10 ±1 seconds.		no looseneed or cut off.						
	1 257 23333 25 27 23 23 23 23		. High Voltage Dice						

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Bending	The capacitor is held in such a manner so that axis of the lead is vertical. As mass applying a force of 5N (for Ø 0,6mm lead) or 2,5N (for Ø 0,5mm lead) is then suspended from the end of the lead. The body of the capacitor is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then returned to its initial position over the same period of time. This operation constitutes one bend. The lead shall be subjected to a total of 2 alternating bends in to opposite directions.	The lead shall be no broken.			
Endurance characte	ristics and test methods.				
Solderability	Solder temperature: 235 ±5°C Immersion time; 2 ± 0,5 seconds Immersion speed: 25 ± 6mm/s	A new uniform coating of the surface being imr	of solder shall cover a minimum of 95% mersed.		
	Frequency range: 10~55Hz.	Apperance	No visible damage		
Vibration	Amplitutde (total excursion); 1,5mm	Capacitance change	Within specified tolerance		
VIDIALIOII	Total duration: 6hours. This motion shall be aplied for 2 hours in aech of three mutually perpendicular directions.	Quality factor or dissipation factor	Refer to clause 5.1.2		
	Solder temperature and immersion time: 260 ± 5°C, 10 ± 0,5 seconds.	Apperance	No visible damage		
Resistance to Soldering Heat	The immersing depth shall be a position 1,27mm from the seating plane.	Capacitance change	± 2,5% or ± 0,25pf (whichever is greater, for class I). ± 5% (for Y5P and YR). ±15% (for Y5U and Z5U). ±20% (for Y5V and Z5V).		
	Post treatment: The capacitor shall be preversed at the standard atmospheric condition for 24 ±	Voltage Proof (for			
0.1(2hours.	between leads only)			
Solvent resistance	The capacitor shall be immersed into isopropylalcohol. For 30 ± seconds.	Apperance	No visible damage legible marking		

										Part No.:
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							Apperance			ble damage			
									± 5% o	or ± 0,5pf (wh	ichev	er is the greater for	class 1)
								Change	± 10%	(Y5P and YF	₹)		
			Capacitarioc O	riange	± 20%	(Y5U and Z5	5U)						
			4		± 30%	(Y5V and Z5	δV)						
	The capacitor shall be placed in the test chamber at temperature of -25 ± 2°C for 30minutes then at room temperature for 3 minutes at 85 ±2°C (125 ±2°C for YR) for								Q ≥ 20	0 + 10Cr (fo	r Cr <	<10pf	
Temperature Cycle			temperature for						Q ≥ 27	5 + 5/2Cr (fo	or 10p	of ≤ CR<30pf	
Tomporataro Oyolo			e subjected to a				Quality facto		Q ≥ 35	0 (for Cr ≥ 3	80pf)		
		•	ed at the standar	•		•	dissipation fa	actor	5% ma	ax. (Y5V & Z5	5V)		
		·		•						ax. (Y5P, YR,		& Z5U)	
										nax. (SBBLC	;)		
							Insulation Resis	istance		I Ω min.			
									Ω min. (SBBL				
							Voltage pro		For between leads only.				
							Apperance			ble damage			
				0.4				Capacitance Change As the same					
			be stored for 500				Q or DF		As the				
Damp Heat	relative	tive humidity of 90 to 95%. Post treatment: The capacitor shall be preseved for 1 to 2 hours at the standard atmospheric condition.				2500M Ω min (Class I)							
		2 r	nours at the stan	dard atmospher	ic condition.		Insulation Resis		recent 12 mm (class ii)				
										500M Ω min (Class III) For between leads only.			
							Voltage pro		For bei	tween leads	oniy.		
							Apperance						
	The	The voltage that is equal to 200% rated voltage (for 50V and 500V capacitors), or Quality factor of Quality factor of the control of the cont						_					
Endurance			or 1KV~3KV cap				dissipation fa				The	e same us before	
Endurance	SBBL	C) shall be appli	ed continuously t	•	•	f 85 ± 3°C (125	dissipation la	actor					
	± 3°C for YR) for 1000 ⁺⁴⁸ hours. Insulation Resis						istance						
	Voltage proof						oof				Cura a m I I i arla V	Valtaria Dias	
											Super High	_	
										Ceramic (Capacitor		
												Part No.:	123006
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APPD: Sc	humi			FINISH	Jamy		Sheet	t No.		6 from 1	4	Custoffici.	









Structure and ROHs Materail request

The marking of class I temperature characteristics is the color block on top of the capacitor

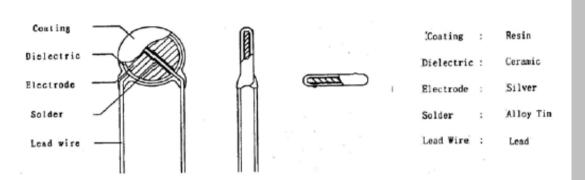
Temperature	C	Ο Δ	SL					
Characteristics	Bla	None						
The marking of class II & III temperature characteristics is symbols specified in following table:								
Temperature Y5P Y5U / Z5U Y5V / Z5V YR								
Characteristics	Black	E	F	HRR&R				

Capacitance

When rated capacitcance is under 1ßßpf the capacitance marking is value being rated capacitance in unit pf. When rated capacitance is 100pf or over the capacitance marking is made in third digit method.

Tolerance:

The tolerance									
table.									
Tolerance:	±20%								
Symbol	М								
The tolerance									
following table.									
Tolerance:	.+80%, -20%								
Symbol	Z								
Dotad Valtage	Catad Valtaga								



Components	Material	ROHS request	Remark		
Coating	Resin	Cd <100ppm;			
Dielectric	Ceramic	Pb <100ppm;	Appendix 1; SGS report (Availbale as customer request or See Appendix		
Electrode	Silver	LIC Ctr DDD DDDC			
Solder Alloy tin		HG, Ctr PBBs, PBDEs, N.D	1		
Lead Wire	Lead	1,,,,,			

Rated Voltage

When rated voltage is 50V the voltage marking is symbol "____" under capcitance marking.

When rated voltage is 500V the voltage marking is symbol "__" over capcitance marking.

When rated voltage is 1000Vor over, the voltage marking is symbols 1KV, 2KV, 3KV, 6KV..... over capacitance marking.

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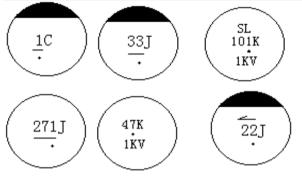


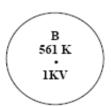




Example of marking (Class I)

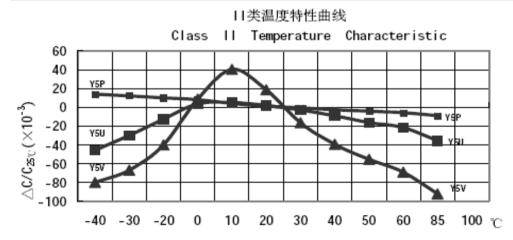
Example of marking (Class II & III) over 1000 Volt

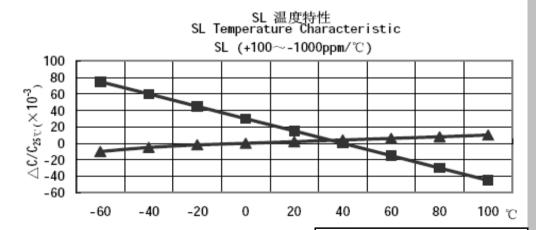






Typical Characteristics Graph





Super High Voltage Disc Ceramic Capacitor							
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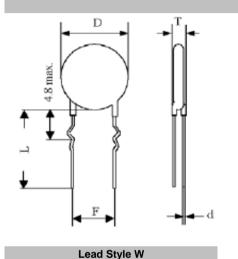


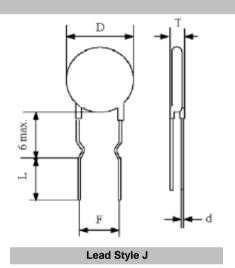


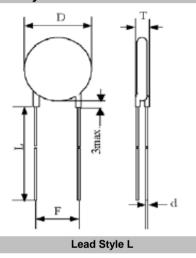


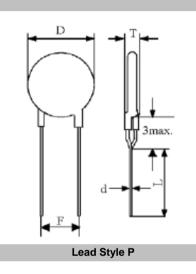
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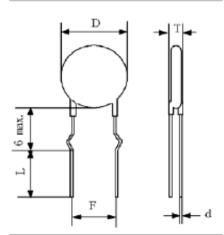
Lead Style











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Customer:

Lead Style K

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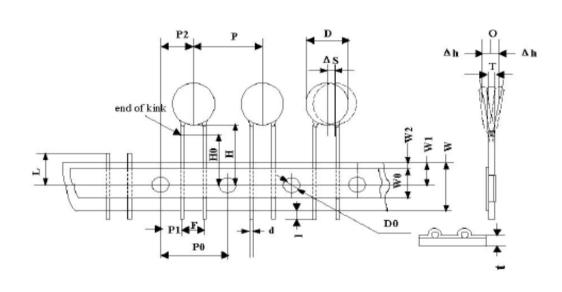


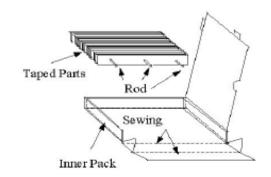






Packing Style F





Symbol	Dimension (mm)
P0	12,7 ±0,2
P0	12,7 ±1,0
F	5,0 +0,5/-0,2
P1	3,85 ±0,4
P2	6,35 ±0,4
H0	16,0 ±0,5
Н	20,0 ±0,5
W	18,0 ±0,5
W0	8,0 min
W1	9,0 ±0,3
W2	3,0max.
t	0,7 ±0,2
D	To comply with individual sheet
D0	4,0 ±0,2
d	To comply with individual sheet
İ	2,0 max.
L	11 max.
Т	To comply with individual sheet
ΔS	0,5 max
ΔΗ	0,5 max

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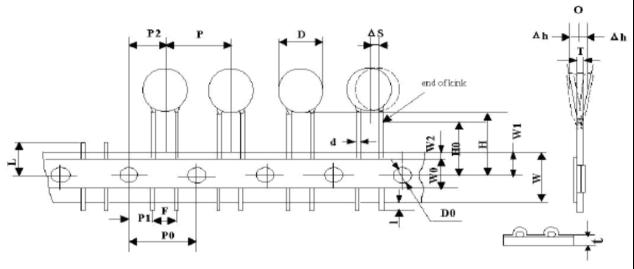




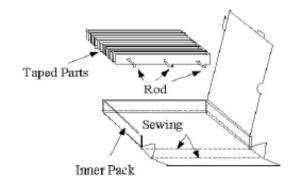




Packing Style V



Symbol	Dimension (mm)
P0	15,0 ±0,2
P0	15,0 ±1,0
F	7,5 +0,5/-0,2
P1	3,75 ±0,4
P2	7,5 ±0,4
H0	16,0 ±0,5
Н	20,0 ±0,5
W	18,0 ±0,5
W0	11,5 min
W1	9,0 ±0,3
W2	3,0max.
t	0,7 ±0,2
D	To comply with individual sheet
D0	4,0 ±0,2
d	To comply with individual sheet
I	2,0 max.
L	11 max.
Т	To comply with individual sheet
ΔS	0,5 max
ΔΗ	0,5 max



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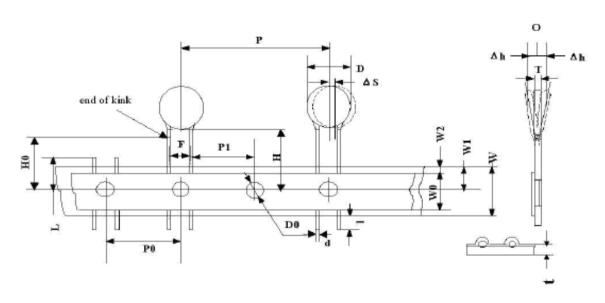


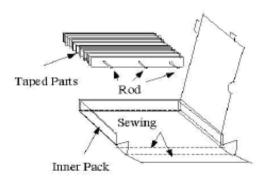






Packing Style U





Symbol	Dimension (mm)
P0	12,7 ±0,2
P0	25,4 ±1,0
F	10,0 +0,5/-0,2
P1	7,7 ±0,4
P2	
H0	16,0 ±0,5
Н	20,0 ±0,5
W	18,0 ±0,5
W0	11,5 min
W1	9,0 ±0,3
W2	3,0max.
t	0,7 ±0,2
D	To comply with individual sheet
D0	4,0 ±0,2
d	To comply with individual sheet
I	2,0 max.
L	11 max.
Т	To comply with individual sheet
ΔS	0,5 max
ΔΗ	0,5 max

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DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	٢
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Ordering Informations

Serie

Ī	Range	Tolerance Code	Material Code	Voltage Code	Lead Length	Lead Style	Lead Pitch	Lead Diameter	ROHS	Packing Code
- 1		0000	0000					Diamoto.		0000

123006

102	K	5P	М	11	L	D	8	R	BU

102= 1000pf K= ±10% 5P= Y5P M= 15	KV 11 = 11mm	L= Style L	D= Pitch 10mm	8= 0,80mm	R= ROHS Conform	BU= Bulk Ware
<u> </u>	25= 25mm	P= Style P			N= NON ROHS	TF= Tape Style F
		W= Style W			Conform	TV= Tape Style U
		J = Style J				TU= Tape Style U

K= Style K

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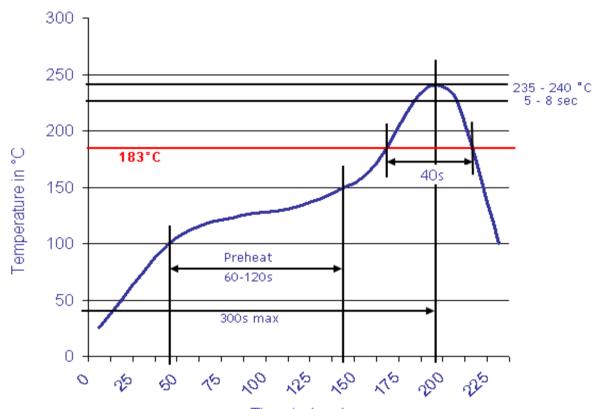






Soldering Profile Curve

Classification Reflow Profile (JEDEC J-STD-020C)



Time in (sec)

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