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# DATA SHEET

## **Super High Voltage Disc Ceramic Capacitor**

## Serie: I23003

**Range** 151= 150pf

Voltage 8000 Volt

Body Diam. 8,5mm

Body Thickn. 8,0mm

**Tolerance** K= ±10%

Material Character. 5P

Pitch 10mm

											Voltage Disc Capacitor
_										Serie No.:	123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customer:	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	1 from 14	Customer.	
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#### Features

Wide rated Voltage range, wide nominal capacitance range Flame retardent, insulating coating applied **Recomended Application** Filter circuit of high voltage power

High voltage circuit of television set and monitor

High voltage circuit of various electronic equipment

Characteristics	Temp.Char. SL	Temp.Char. Y5P	Temp.Cl	nar. Y5U	Temp.C	har. Y5V			
Operating Temperature		30°C	~ +85°C						
Rated Voltage	4KVDC ~ 6KVDC	15KVDC	4KVDC ~ 15KVDC						
Withstanding Voltage		1,5 times re	lated voltage	ated voltage					
Capacitance	Within the specified tolerance, testing at 25°C, 1Vrms and 1KHz (at 1MHz for SL products)								
Capacitance	10 ~ 330pf	100 ~ 2200pf 470 ~ 3300pf		1000 ~	1000 ~ 10000pf				
Dissipation Factor	Cr<30pf, Q≥ 400+20Cr Cr≥30pf, Q≥1000	tg ≤ 2,5%	tg ≤ 3,5%						
Insulation Resistance		Charge at 500VDC for 6	60 seconds, Rj ≥ 1000MΩ						
Temperature	Temperatur Cha	rarcteristics Code	SL	Y5P	Y5U	Y5V			
Characteristics	Temperatur Coe	fficient (10-6 /°C)	. +100 ~ - 1000 10-6/°C	. ± 10%	.+22 ~ +56%	.+22 ~ +82%			

#### **Rated Capacitance**

The first and second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier. The capacitance unit is pf,

#### **Capacitance Tolerance**

Letter Sym	bol	Capacitanc	e Tolerance		Lett	ter Symbol	Capa	citance Toler	ance			
С		±0,2	25pf			K		±10%			Super High Voltage Disc	
D		±0,	5pf			М		±20%			• •	-
J		±5	5%			Z		.+80 ~ -20%			Ceramic	c Capacitor
											Part No.:	123003
DRW:	Jason	CHKD	Wilson	MA	TL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customori	
APPD:	Schumi			FIN	ISH	Jamy		Shee	t No.	2 from 14	Customer:	
www.edcon-co	omponents.co	m								e	email: info@edco	n-components.com





Standard atmospheric condition Temperature: 15~35°C Relative Humidity: 45~75% Atmospheric pressure: 86~106KPa (860~1060mbar Operating and storage temperature range Operating Temperature: Lowest Operating Highest Operating Temperature Temperature Temperature Characteristics SL . -25°C .+85°C . -25°C .+85°C COH Y5P . -25°C .+85°C Y5U . -25°C .+85°C Y5U . -25°C .+85°C Y5V . -25°C .+85°C Z5U 10°C .+85°C Z5V 10°C .+85°C YR . -25°C .+125°C Storage Temperature Range: -10 to + 40°C

Wilson

MATL:

FINISH



	Characteristic			hods I test methods					
ſ	Capacitano toleranc	ce &	The Ca and 1V	pacitance shall l rms (Class1), 1k Vrms (for Calss	(Hz and 1Vrms (		Refer to	individual she	et
							Q≥400+2	20Cr (forCr<30p	of)
							Q≥100	0 (forCr<30pf)	
							Cr-rated cap	acitance in unit	of pf
	Quality fact	or or	The	quality factor o	r dissipation fa	ation factor shall be		or Y5P,Y5U and	d Z5U
	dissipation f	actor	me	asured at the s	ame condition	s ab above		max. (for YR)	
							3,5% max.	(for Y5V and Z	5U)
							5%max. (for S	SBBLC Y5V and	d Y5U)
							3,5%max.	(for SBBLC Y5	δP)
	Insulatio Resistan		voltage	ulation resistanc (for Vr≤500VDC VDC)within 50±	;); 500VDC (for		1000M Ω min Ω min	(for SBBLC)	1000M
	Voltage Pr	roof	540V a 1000V 1 3000\ shall b voltage 1300	oltage of 300% and 500V) 200% to 2000V), 175% /), or 150% rate e applied betwee s of 250% rated DV ( fort 500V, 11 veen leads conn	rated voltage (fo a rated voltage (fo d Voltage (for Do en leads for 1 to voltage (for 50) KV and over) sha	or rated voltage for rated voltage CG or SBBLC) 5seconds. The / capacitors) or all be applied	age tage No breakdown or flashove .C) The ) or sd		
				wrapped on env	elope for 1 to 5 s	seconds.		jh Voltage ic Capacito	
							Part No.:	123003	3
	Wilson	TOLEF	RANCE	Mason	DATE	30.04.2011	Customer:		
	Jamy			Shee	neet No. 3 from 14				

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Jason

Schumi

DRW:

APPD:

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	The capacitor shall be kept for enough time to reach thermal equilibrium at special temperature of each step in the following table.	
	The capacitance measurement shall be made only at the thermal equilibrium of each step.	Class I
	Step Temperature Step Temperature	Temperature coefficient:
	1 20 ± 2°C 4 85 ±2°C (125±2°C for YR)	Refer to specification sheet
	2 $25 \pm 2^{\circ}$ C 5 $20 \pm 2^{\circ}$ C	Capacitance drift:
	3 $20 \pm 2^{\circ}C$	Within $\pm 1\%$ or $\pm 0,05pf$
	For temperature characteristics SL the steps 1 and step 2 may be omitted.	(Whichever is greater)
	The temperature coeffizient and the capacitance drift shall be calculated by the following formulas. ( Cm - Co )	
	=	Class II & III
_	 Со (Т- То) х10 <sup>6</sup> (ррт/°С)	Temperature Permitting
Temperature	$C_0 - C_1$ $C_5 - C_0$ $C_5 - C_1$	Characteris capacitance
Characteristics	= Or	tics change
	Co Co Co	Y5P ± 10%
	Where	YR ± 15% to -30%
	Co Capacitance at step 3	Y5U ± 22% to -56%
	Cm Capacitance at step 2 and/or step 4	Z5U ± 22% to -56%
	C1,C5 Capacitance at step 1 and step 5	Y5V ± 22% to -82%
	To Measuring temperature at Step 3	Z5V ± 22% to -82%
	T Measuring temperature at Step 2 and /or step 4	
	Pre-tratement:	
	The capacitor shall be stored at a temperature of 55 ±2°C and a relative humidity of 20% or less for 16 to 24 hours.	
	And then the capacitor shall be allowed immediately to cool in container using appropriate dryer such as activated carbon, silica gel	
Robustness of	The capacitor body shall be held in such a manner so that axis of the lead is vertical. The tensile force of 10N (for Ø 0,6mm	lead) The capacitor shal be no
Termination	ot 5N (for Ø 0,5mm lead) shall be applied to the lead in a direction of ist axis and acting in a direction away from the body of	of the broken and the lead shall be
rennination	capacitor for 10 ±1 seconds.	no looseneed or cut off.
		Super High Voltage Disc
		Ceramic Capacitor
		Part No.: <b>123003</b>
		Fait NO 123003

_											Fall NO	123003
	DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customor	
	APPD:	Schumi			FINISH	Jamy		Shee	t No.	4 from 14	Customer:	
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Bending	The capacitor is held in such a manner so that axis of the lead is vertical. As mass applying a force of 5N (for Ø 0,6mm lead ) or 2,5N (for Ø 0,5mm lead) is then suspended from the end of the lead. The body of the capacitor is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then returned to its initial position over the same period of time. This operation constitutes one bend. The lead shall be subjected to a total of 2 alternating bends in to opposite directions.	The lea	ad shall be no broken.
Endurance characte	eristics and test methods.		
Solderability	Solder temperature: 235 ±5°C Immersion time; 2 ± 0,5 seconds Immersion speed: 25 ± 6mm/s	A new uniform coating of the surface being imr	of solder shall cover a minimum of 95% nersed.
	Frequency range: 10~55Hz.	Apperance	No visible damage
Vibration	Amplitutde (total excursion); 1,5mm	Capacitance change	Within specified tolerance
VIDIAUOII	Total duration: 6hours. This motion shall be aplied for 2 hours in aech of three mutually perpendicular directions.	Quality factor or dissipation factor	Refer to clause 5.1.2
	Solder temperature and immersion time: $260 \pm 5^{\circ}$ C, $10 \pm 0.5$ seconds.	Apperance	No visible damage
Resistance to Soldering Heat	The immersing depth shall be a position 1,27mm from the seating plane.	Capacitance change	$\pm$ 2,5% or $\pm$ 0,25pf (whichever is greater, for class I). $\pm$ 5% (for Y5P and YR). $\pm$ 15% (for Y5U and Z5U). $\pm$ 20% (for Y5V and Z5V).
	Post treatment: The capacitor shall be preversed at the standard atmospheric condition for $24 \pm 2$ hours.	Voltage Proof (for between leads only)	
Solvent resistance	The capacitor shall be immersed into isopropylalcohol. For $30 \pm$ seconds.	Apperance	No visible damage legible marking

											Voltage Disc Capacitor 123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011		123003
APPD:	Schumi			FINISH	Jamy		Shee	t No.	5 from 14	Customer:	
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								A == = = = = =		Neviai		ila la una arteira ar		
								Apperanc	ce		ble damage Leg			
												ever is the greater	for class 1)	
								Capacitance C	Change		(Y5P and YR)			
									0		(Y5U and Z5U)			
		The	capacitor shall I	be placed in the	test chamber at	temperature of	$-25 \pm 2^{\circ}$ C for				(Y5V and Z5V)			
				om temperature f							0 + 10Cr ( for Cı	•		
Temperature C	vcle			temperature for		,	,				5 + 5/2Cr ( for 1	•		
	<b>j</b>			subjected to a t				Quality facto			0 (for $Cr \ge 30pf$ )	)	r is the greater for class 1) Opf ≤ CR<30pf & Z5U) same us before Super High Voltage Disc Ceramic Capacitor	
			•	ed at the standar	-			dissipation fa	actor	5% ma	x. $(Y5V \& Z5V)$ x. $(Y5P, YR, Y5U \& Z5U)$ nax. $(SBBLC)$ $\Omega$ min. $\Omega$ min. $\Omega$ min. $(SBBLC)$ ween leads only. ble damage same same $\Omega$ min (Class I )			
			·		·				3% max. (Y5P, YR, Y5U & Z5U)					
										7,5% m	nax. (SBBLC)			
								Insulation Resi	istance	1000M	Ω min.			
								500M Ω min. (SBBLC)						
								Voltage pr	oof	For bet	ween leads only	/.		
							Apperanc	ce	No visil	ble damage				
								Capacitance C	Ŭ	As the				
				e stored for 500				Q or DF	-	As the				
Damp Hea	t	relative		o 95%. Post trea			reseved for 1 to			2500M	$\Omega$ min (Class I )	)		
		2 hours at the standard atmospheric condition. Insulation Resistance 1000M $\Omega$ min (Class II)												
									500M (	ם min (Class III )	)			
								Voltage pr	oof	For between leads only.				
								Apperanc	ce					
		Tho	voltago that is o	qual to 200% rat	ad voltage (for F	50 / and $500$ / a	apacitors) or	Capacitance C	Change					
				or 1KV~3KV cap				Quality facto			т	he same us hefore	2	
Endurance	•		• •	•		•		dissipation fa	actor		I	ne same us belore	,	
		SBBLC) shall be applied continuously to the capacitor at temperature of $85 \pm 3^{\circ}$ $\pm 3^{\circ}$ C for YR) for 1000 <sup>+48</sup> hours.												
								Voltage pr	roof					
								voltage pr	001	J			-	
												Ceramie	c Capacitor	
												Part No.:	123003	
DRW:	Jas	on	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DA	TE	30.04.2011			
APPD:	Sch		-		FINISH	Jamy		Shee	et No.		6 from 14	Customer:		

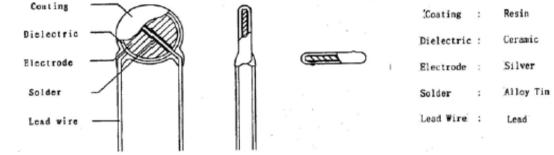




#### **Structure and ROHs Materail request**

The marking of class I temperature characteristics is the color block on top of the capacitor

Temperature	C	ΟΔ	S	βL								
Characteristics	Bla	ack	Nc	one								
The marking o	of class II & III	temperature ch	naracteristics is	s symbols								
specified in fo	llowing table:											
Temperature	Y5P	Y5U / Z5U	Y5V / Z5V	YR								
Characteristics	Black	E	F	HRR&R								
Capacitance	pacitance											
When rated cap	ated capacitcance is under 1ßßpf the capacitance marking is value											
peing rated capacitance in unit pf. When rated capacitance is 100pf or over the												
capacitance ma	arking is made in	third digit metho	od.									
Tolerance:					_							
The tolerance	marking for C	lass I is the sy	mbols specified	d in following								
table.												
Tolerance:	± 0,25pf	±0,5pf	±5%	±10%	±20%							
Symbol	С	D	J	К	М							
The tolerance	marking for C	lass II & III is t	he symbols sp	ecified in								
following table	Э.											
Tolerance:	± 10%	± 20%	.+50%, -20%	.+100%, 0%	.+80%, -20%							
Symbol	К	М	SL	Р	Z							
Datad Valtaga												



Components	Material	ROHS request	Remark		
Coating	Resin	Cd <100ppm;			
Dielectric	Ceramic	Pb <100ppm;	Appendix 1; SGS report		
Electrode	Silver		(Availbale as customer request or See Appendix		
Solder	Alloy tin	HG, Ctr PBBs, PBDEs, N,D	1		
Lead Wire	Lead	N,D			

Rated Voltage

When rated voltage is 50V the voltage marking is symbol "\_\_\_\_" under capcitance marking.

When rated voltage is 500V the voltage marking is symbol "\_\_" over capcitance marking.

When rated voltage is 1000Vor over, the voltage marking is symbols 1KV, 2KV, 3KV, 6KV..... over capacitance marking.

									Voltage Disc Capacitor		
										Part No.:	123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customer:	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	7 from 14	Customer.	

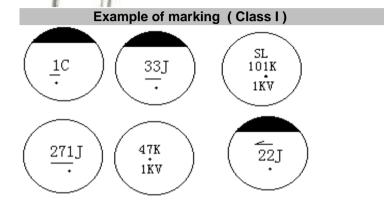
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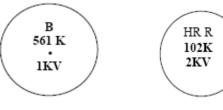
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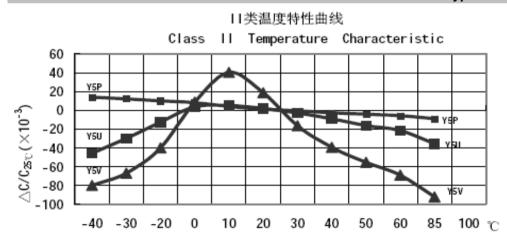
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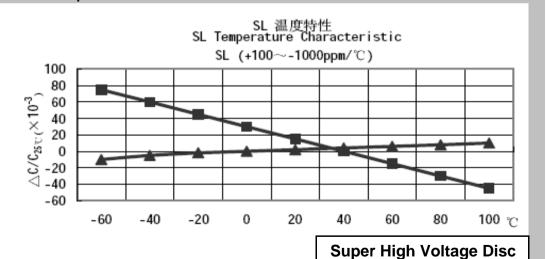






**Typical Characteristics Graph** 





										Part No.:	123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customori	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	8 from 14	Customer:	
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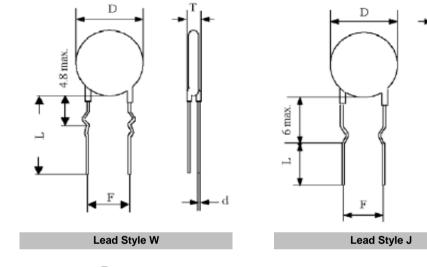
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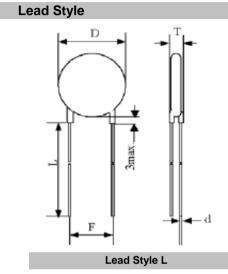
**Ceramic Capacitor** 

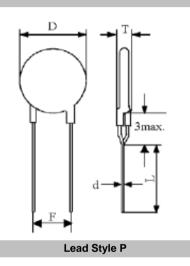


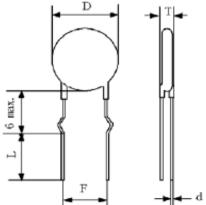












	,     _→  →	d d									Voltage Disc Capacitor
Le	ead Style K									Part No.:	123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customer:	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	9 from 14	Customer.	
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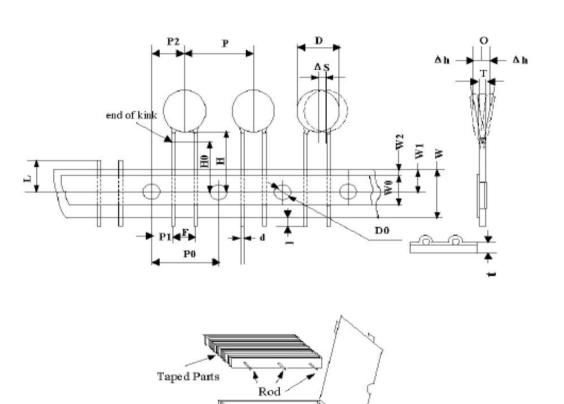
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Packing Style F



Symbol	Dimension (mm)
P0	12,7 ±0,2
P0	12,7 ±1,0
F	5,0 +0,5/-0,2
P1	3,85 ±0,4
P2	6,35 ±0,4
H0	16,0 ±0,5
Н	20,0 ±0,5
W	18,0 ±0,5
W0	8,0 min
W1	9,0 ±0,3
W2	3,0max.
t	0,7 ±0,2
D	To comply with individual sheet
D0	4,0 ±0,2
d	To comply with individual sheet
	2,0 max.
L	11 max.
Т	To comply with individual sheet
$\Delta$ S	0,5 max
$\Delta H$	0,5 max

		Inner Pack	ewing							Ceramic	Voltage Disc Capacitor
										Part No.:	123003
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customer:	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	10 from 14	Customer.	
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Packing Style V







0 P2 P D Δh end of kink d  $\mathbf{D}\mathbf{0}$ P1 PO

Taped Parts	
Rod	
Sewing	
Innar Dock	1

	Symbol	Dimension (mm)
	P0	15,0 ±0,2
	P0	15,0 ±1,0
Δh	F	7,5 +0,5/-0,2
[	P1	3,75 ±0,4
	P2	7,5 ±0,4
	H0	16,0 ±0,5
	Н	20,0 ±0,5
	W	18,0 ±0,5
	W0	11,5 min
	W1	9,0 ±0,3
	W2	3,0max.
	t	0,7 ±0,2
	D	To comply with individual sheet
	D0	4,0 ±0,2
	d	To comply with individual sheet
	I	2,0 max.
	L	11 max.
	Т	To comply with individual sheet
	$\Delta$ S	0,5 max
	$\Delta H$	0,5 max

ped Parts	Rod
$\mathcal{L}$	Sewing
Inner Pack	

	• •	h Voltage Disc c Capacitor
	Part No.:	123003
1 2011		

										raititon	120000
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customor	
APPD:	Schumi			FINISH	Jamy		Shee	t No.	11 from 14	Customer:	
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Packing Style U







0 Р Δh Δh Δs end of kink F P1 Ξ Ĥ 3 DO H.  $\mathbf{P0}$ 

Symbol	Dimension (mm)
P0	12,7 ±0,2
P0	25,4 ±1,0
F	10,0 +0,5/-0,2
P1	7,7 ±0,4
P2	
H0	16,0 ±0,5
Н	20,0 ±0,5
W	18,0 ±0,5
W0	11,5 min
W1	9,0 ±0,3
W2	3,0max.
t	0,7 ±0,2
D	To comply with individual sheet
D0	4,0 ±0,2
d	To comply with individual sheet
I	2,0 max.
L	11 max.
Т	To comply with individual sheet
$\Delta$ S	0,5 max
$\Delta H$	0,5 max

Taped Parts	Rod
Inner Pack	Sewing

MATL:

FINISH

Wilson

~						NVoltage Disc Capacitor
					Part No.:	123003
Wilson	TOLERANCE	Mason	DATE	30.04.2011	Customer:	
Jamy		Shee	t No.	12 from 14	Customer.	

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Jason

Schumi

CHKD

DRW:

APPD:

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**Ordering Informations** 

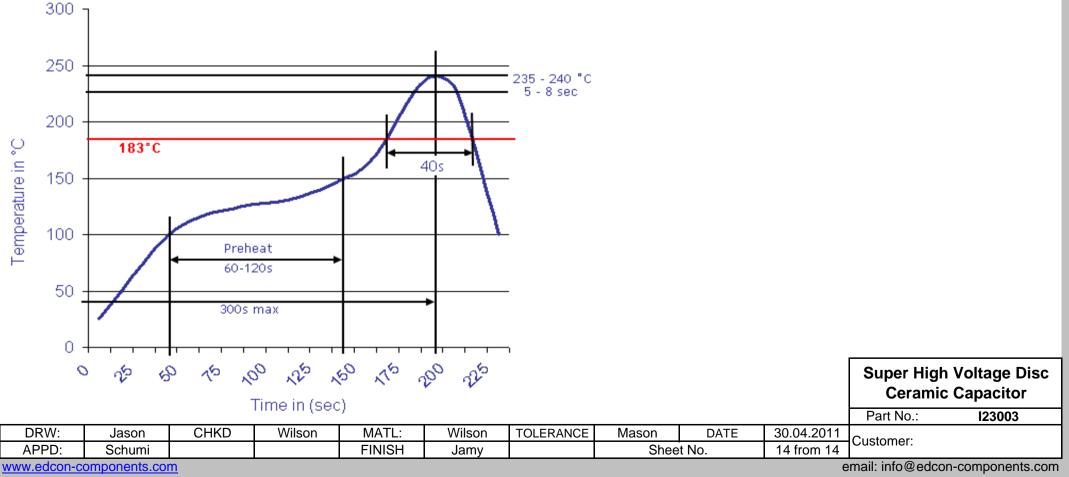
	1		Tolerance	Material					Lead		Pac	king
Serie		Range	Code	Code	Voltage Code	Lead Length	Lead Style	Lead Pitch	Diameter	ROH		ode
							-	-				
123003	-	151	K	5P	Z	11	L	D	7	R	B	U
		<b>151=</b> 150pf	<b>K=</b> ±10%	<b>5P=</b> Y5P	<b>Z=</b> 8KV	<b>11=</b> 11mm	L= Style L	D= Pitch	<b>7=</b> 0,65mm	R= RO		Bulk
			N= 11070	••• = •••				10mm	,	Confo		are
						<b>25=</b> 25mm	P= Style P			N= NC ROH		Tape
							-	-		Confo		le F Tape
							W= Style W			Como		le U
												Tape
							<b>J=</b> Style J				Sty	le Ú
							K= Style K					
											Super Hig	gh Voltage Di
											Ceram	ic Capacitor
											Part No.:	123003
DRW:	Ja	son Cl	HKD Wi	ilson M	ATL: Wi	Ison TOLE	RANCE Ma	ason DA	ATE 30.04	.2011		120000
APPD:		numi				imy		Sheet No.		om 14	ustomer:	
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**Soldering Profile Curve** 

## Classification Reflow Profile (JEDEC J-STD-020C)



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