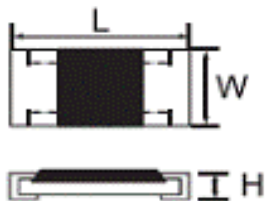


EDCON-COMPONENTS



Thick Film Chip Resistor Tolerance 1%

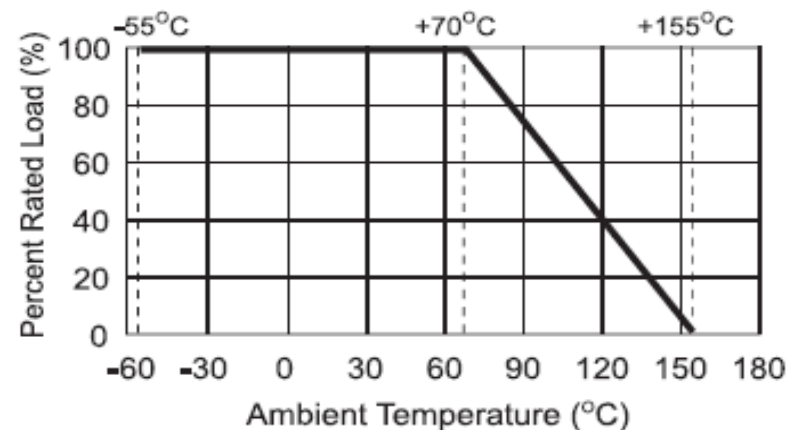
Performance Specifications

Temperature Coefficient	1Ω ~ 10Ω +/-400PPM/°C 11Ω ~ 100Ω +/-100PPM/°C >100Ω +/-100PPM/°C
Short Time Overload	5% +/-2,0% +0,1Ωmax.
Insulation Resistance:	Min 1000Mega Ω
Dielectric Withstanding Voltage	No evidence of Flashover, mechanical damage, arcing or insulation breakdown
Terminal Bending	./-(1,0% +0,05Ω) max.
Soldering Heat	Resistance change rate is +/- (1% + 0,05Ω)max.
Solderability	Min. 95% coverage
Temperature Cycling	./-5% : +/- (1,0% + 0,05Ω) max.
Humidity (Steady State)	./-5% : +/- (3,0% + 0,01Ω) max.
Load Life in Humidity	./-5% : +/- (3,0% + 0,01Ω) max.
Load Life	./-5% : +/- (3,0% + 0,01Ω) max.
Standard Operating Temperature	. -55°C ~ +155°C
Tolerance Informations	E24 Series 2% / 5% / 10%

Features

Small Size and light weight
Suitable for both flow and reflow soldering
Reduction of assembly costs

Dearting Curve



Test Methods of JIS 5201-1

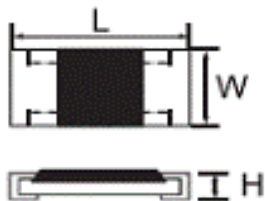
Flame Retardant: (JIS 5201-1 7.12)	Resistor shall resist flaming or arcing when overloaded up to 16 times RCWV or max. RCWV whichever is lesser. Lit burner and adjust to procedure a blue flame 38mm in height & a max. 127mm flame from the burner tube. Resistor is supported from ist lead at 45°C from the horizontal so that the lower end of resistor is on the top of blue flame. The resistor is placed on this test flame for 15sec. Keep cool for 15sec. Repeat this procedure for 5 times.
------------------------------------	---

**CHIP Resistor Size 2010
Tol. 1%**

Part No.: **X30025**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009	Customer:
APPD:	Schumi			FINISH	Jamy		Sheet No.	1 from 7		

EDCON-COMPONENTS



Test Method of JIS C 5201-1 Thick Film Chip Resistor Tolerance 1%

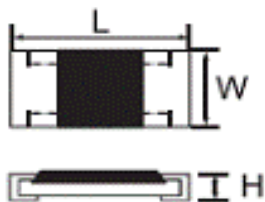
Temperature Coefficient (JIS C 5201-1 4.8)	Natural Resistance change per temperature degree centigrade R1: Resistance value at room temperature (t1) R2: Resistance value at room temperature plus 100°C (t2) Test pattern: Room temperature (t1), Room temperature +100°C (t2)
Short time overload (JIS C5201-1 4.13)	Permanent resistance change after application of a potential of 2,5 times RCWV for 5sec.
Insulation resistance (JIS-C 5201-1 4.6)	Apply 500VDC between protective coating and termination for 1min. Then measure 100V DC specially for resistor network.
Dielectric Withstanding Voltage (Voltage proof) (JIS-C 5201-1 4.7)	Resistors shall be clamped in the method use a metal foil shall be wrapped closely around the body of the resistor. After that shall be tested at AC potential respectively specified in the given list of each product type for 60 +10/0 secs. For cement fixed resistors, the testing voltage is 1000Volts. For Chip resistors, testing voltage is 500 Volts.
Pulse Overload (JIS-5202 5.8)	Resistance change after 10.000 cycles (1sec "ON", 25sec "OFF") at 4times of RCWV or maximum overload:
Terminal Strength (JIS-5201-1 4.16)	Direct Load: Resistance to a 2,5Kgs.. Direct load for 10sec in a direction of the longitudinal axis of terminals leads. Twist Test. Terminal leads shall be bent trough 90° at the point of about 6mm grom the body of the resistor and shall be rotated trough 360° about the original axis of the bent terminal in alternating direction for a total of 3 rotations.
Terminal Strength (JIS-5201-1 4.16)	(Applicable for Resistor Network) Tensile: 1KG., 30sec., Bending: 500g, 2 times
Terminal Strength (JIS-5201-1 4.33)	(Applicable for CHIP Resistors) Twist of the test board: X/Y = 3/90mm for 10sec.
Vibration (JIS-5201-1 4.22)	X, Y, Z, each direction 2 hours, 10~55 ~ 10Hz / min. All amplitude 1,5mm
Soldering Heat (JISC 5201-1 4.18)	(Applicable for Chip Resistors) Dip the resistor into a solder bath having a temper. of 260 +/-5°C and hold it for 10 +/-1sec. Lead(Pb) free temp.: 260 +/-3°C, 5 + 1/-0sec.
Resistance to Soldering Heat (JISC 5201-1 4.18)	Permanent Resistance change when leads immersed to a point 2.0 ~ 2,5mm from the body in 350 +/-10°C solder for 3-4 sec. Lead (Pb) free temper.: 260 +/-3°C, 5 + 1/-0sec.
Solderability (JISC 5201-1 4.17)	The area covered with a new, smooth clean , shiny and continious surface free from concentrated pinholes. Test temperature of solder: 235 +/-5°C; Dwell time in solder: 3~5sec. Lead (PB) free temper.: 245 +/-3°C; Dwell time in solder: 2~3sec.
Resistance to solvent (JIS5201-1 4.29 /4.30)	Specimens shall be immersed in a bath of alcohol completely for 3min. Using ultrasonic test equipment.
Thermal Shock (JIOS 5201-1 4.21)	(Applicable fir Resistor Network) Load V, rom temperature, 30min. Unload, -55°C, 15min. Over 2 hours in room temp. Before measuring.
Rapid Change Temperature: (JIS 5201-1 4.19)	Resistance change after continious 5cycles for duty cycle specified below: Step1: 30min.at -55 +/-3°C / Step2: 10~15min. At room tempeprature. Step3: 30min. At 155 +/-2°C. Step4: 10~15min. At room tempeprature.
Humidity (Steady Style) (JIS 5201-1 4.24)	Temporary resistance change after 240hours exposure in a humidity test chamber controlled at 40 +/-2°C and 50-95% relative humidity.
Load Life in Humidity (JIS 5201-1 7.9)	Resistance change after 1000hrs. (1,5hrs "ON", 0,5hrs "OFF" at RCWV or max. RCWV whichever is lesser in a humidity test chamber controlled at 40 +/-2°C and 90 ~ 95% relative humidity.
Load Life (JIS 5201-1 25.1)	Permanent Resistance change after 1000hours operating at RCWV or max. RCWV whichever is lesser, with duty cycles of 1,5hours "ON" 0,5hours "OFF" at 70 +/-2°C ambient.

**CHIP Resistor Size 2010
Tol. 1%**

Part No.: **X30025**

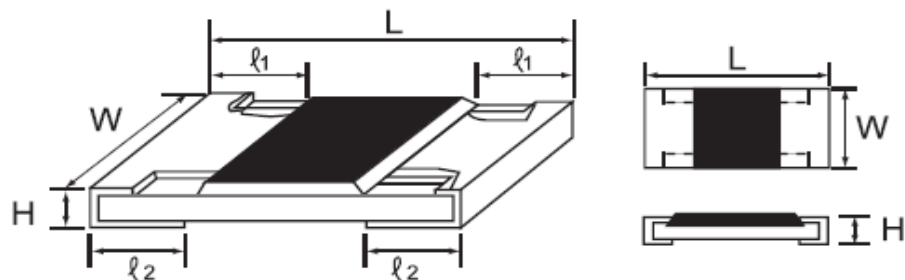
DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009	Customer:
APPD:	Schumi			FINISH	Jamy		Sheet No.	2 from 7		

EDCON-COMPONENTS

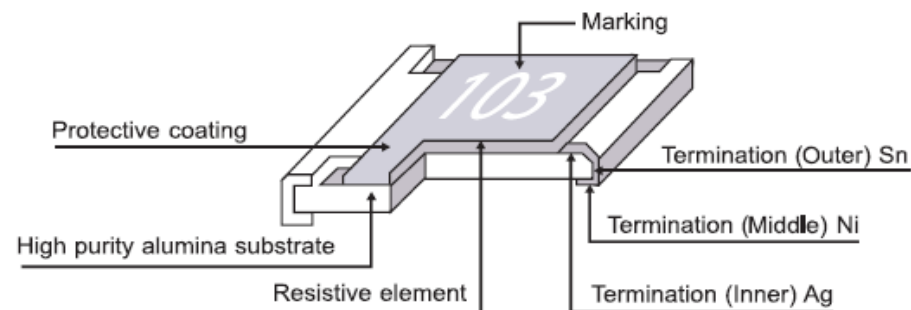


Thick Film Chip Resistor Tolerance 1%

Technical Dimensions



Physical Structure



Size	Power Rating at 70°C	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Tolerance %	Resistance Range	Dimensions (mm)				
							L	W	H	l1	l2
2010 (5025)	3/2W	200V	400V	500V	±1%	10Ω ~ 1MΩ	5,00 ±0,10	2,50 ±0,15	0,55 ±0,10	0,60 ±0,25	0,50 ±0,20

**CHIP Resistor Size 2010
Tol. 1%**

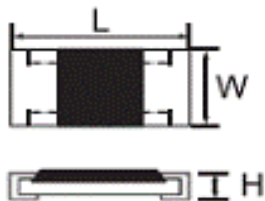
Part No.: **X30025**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009	Customer:
APPD:	Schumi			FINISH	Jamy		Sheet No.	3 from 7		

www.edcon-components.com

email: info@edcon-components.com

EDCON-COMPONENTS



Thick Film Chip Resistor Tolerance 1%

Range Table Row E96 included (E24 dark marking)

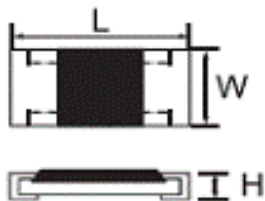
$\Omega / K\Omega$	1,00	1,02	1,05	1,07	1,10	1,13	1,15	1,18	1,20	1,21	1,24	1,27	1,30	1,33	1,37	1,40	1,43	1,47	1,50	1,54	1,58	1,60	1,62	1,65
$\Omega / K\Omega$	1,69	1,74	1,78	1,80	1,82	1,87	1,91	1,96	2,00	2,05	2,10	2,15	2,20	2,21	2,26	2,32	2,37	2,40	2,43	2,49	2,55	2,61	2,67	2,70
$\Omega / K\Omega$	2,74	2,80	2,87	2,94	3,00	3,01	3,09	3,16	3,24	3,30	3,32	3,40	3,48	3,57	3,60	3,65	3,74	3,83	3,90	3,92	4,02	4,12	4,22	4,30
$\Omega / K\Omega$	4,32	4,42	4,53	4,64	4,70	4,75	4,87	4,99	5,10	5,11	5,23	5,36	5,49	5,60	5,62	5,76	5,90	6,04	6,19	6,20	6,34	6,49	6,65	6,80
$\Omega / K\Omega$	6,81	6,98	7,15	7,32	7,50	7,68	7,87	8,06	8,20	8,25	8,45	8,66	8,87	9,09	9,10	9,31	9,53	9,76						
$\Omega / K\Omega$	10,0	10,0	10,1	10,1	11,0	11,3	11,5	11,8	12,0	12,1	12,4	12,7	13,0	13,3	13,7	14,0	14,3	14,7	15,0	15,4	15,8	16,0	16,2	16,5
$\Omega / K\Omega$	16,9	17,4	17,8	18,0	18,2	18,7	18,1	19,6	20,0	20,5	21,0	21,5	22,0	22,1	22,6	23,2	23,7	24,0	24,3	24,9	25,5	26,1	26,7	27,0
$\Omega / K\Omega$	27,4	28,0	28,7	29,4	30,0	30,1	30,9	31,6	32,4	33,0	33,2	34,0	34,8	35,7	36,0	36,5	37,4	38,3	39,0	39,2	40,2	41,2	42,2	43,0
$\Omega / K\Omega$	43,2	44,2	45,3	46,4	47,0	47,5	48,7	49,9	51,0	51,1	52,3	53,6	54,9	56,0	56,2	57,6	59,0	60,4	61,9	62,0	63,4	64,9	66,5	68,0
$\Omega / K\Omega$	68,1	69,8	71,5	73,2	75,0	76,8	78,7	80,6	82,0	82,5	84,5	86,6	88,7	90,9	91,0	93,1	95,3	97,6						
$\Omega / K\Omega$	100	102	105	107	110	113	115	118	120	121	124	127	130	133	137	140	143	147	150	154	158	160	162	165
$\Omega / K\Omega$	169	174	178	180	182	187	191	196	200	205	210	215	220	221	226	232	237	240	243	249	255	261	267	270
$\Omega / K\Omega$	274	280	287	294	300	301	309	316	324	330	332	340	348	357	360	365	374	383	390	392	402	412	422	430
$\Omega / K\Omega$	432	442	453	464	470	475	487	499	510	511	523	536	549	569	562	576	590	604	619	629	634	649	665	680
$\Omega / K\Omega$	681	698	715	732	750	768	787	806	820	825	845	866	887	909	910	931	953	976						

**CHIP Resistor Size 2010
Tol. 1%**

Part No.: **X30025**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009
APPD:	Schumi			FINISH	Jamy		Sheet No.	4 from 7	Customer:

EDCON-COMPONENTS



Thick Film Chip Resistor Tolerance 1% Range Code Body Marking

All case sizes except 0201 and 0402
 5% Tolerance: 10Ω ~ 10MΩ
 1 digit & 2 digit are significant figures of the resistance
 3 digit indicates number of zeros

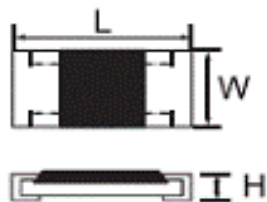
Tol. 1% , Tol. 5%
 0,1Ω ~ 0,99Ω
 R36 = 0,36Ω

Tol. 1%
 1Ω ~ 9,9Ω
 3R30 = 3,30Ω

Tol. 1%
 10Ω ~ 10MΩ
 2432 = 24,3KΩ

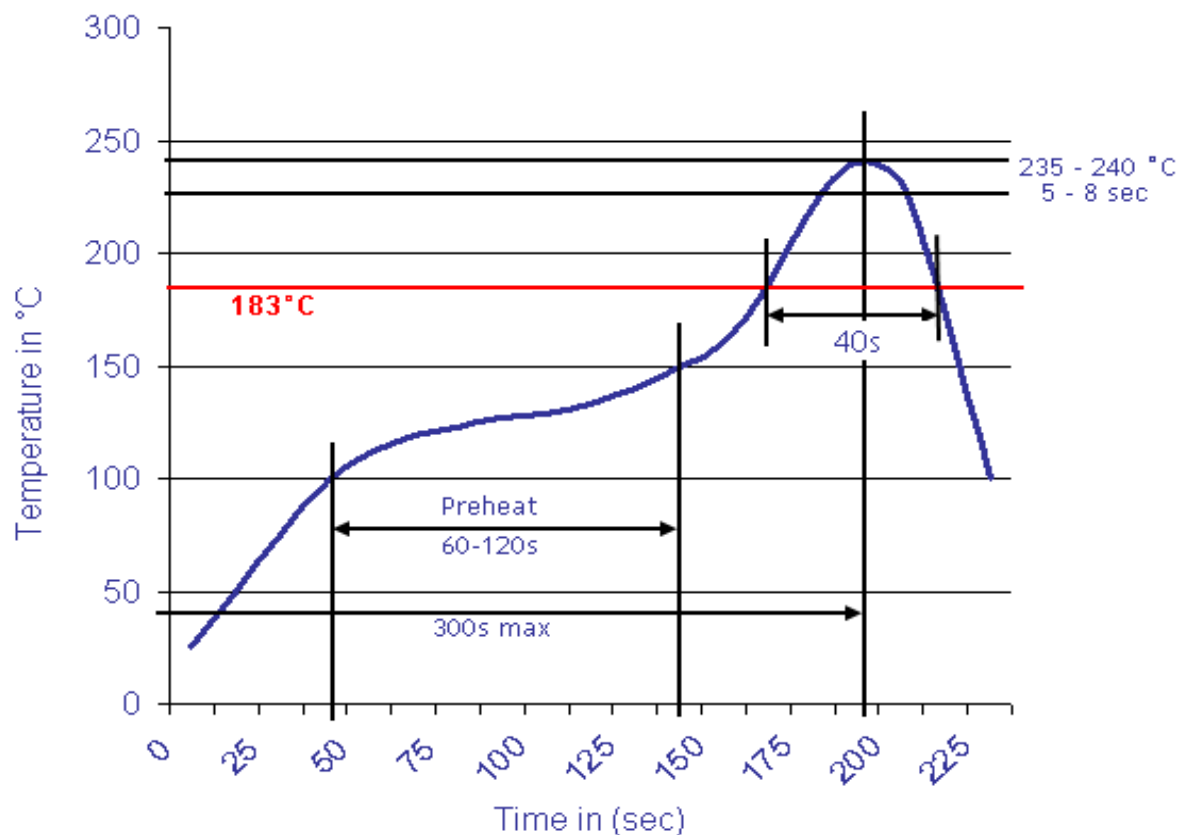
CHIP Resistor Size 2010 Tol. 1%	
Part No.:	X30025
Customer:	

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009
APPD:	Schumi			FINISH	Jamy		Sheet No.		5 from 7



Thick Film Chip Resistor Tolerance 1% Soldering Profile Curve

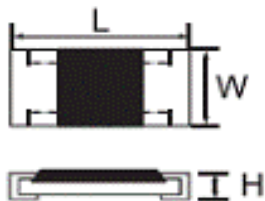
Classification Reflow Profile (JEDEC J-STD-020C)



CHIP Resistor Size 2010	
Tol. 1%	
Part No.:	X30025
Customer:	

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009
APPD:	Schumi			FINISH	Jamy		Sheet No.		6 from 7

EDCON-COMPONENTS



Thick Film Chip Resistor Tolerance 1% Ordering Information Table

Ordering Informations

Serie	Range Code	Tolerance	T.C.R	ROHS	Tape / Reel + Quantity					
X30025	3R30	F	A0	R	T502					

3R30= look Range Table	F= 1%	A0= +/- 100ppm	R= ROHS Conform
2432= look Range Table			N= NON ROHS

T502= Tape
Reel 5000PCS

T103= Tape
Reel 10000PCS

**CHIP Resistor Size 2010
Tol. 1%**

Part No.: **X30025**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	10.04.2009
APPD:	Schumi			FINISH	Jamy		Sheet No.	7 from 7	

Customer: